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221 Fast field solver-programs for thermal and electrostatic analysis of microsystem elements

Vladimir Székely, Márta Rencz

November 1997 I CCAD '97: Proceedings of the 1997 IEEE/ACM international

conference on Computer-aided design

Publisher: IEEE Computer Society

Additional Information: full cliation, abstract.
Full text available: Publisher Site, Pdf (155.98 KB)
Additional Information: full cliation, abstract, references, other by inclease terms.

Bibliometrics: Downloads (6 Weeks): 2, Downloads (12 Months): 15, Citation Count: 1

To solve the problem of fast thermal and electrostatic simulation of microsystem elements two different field solver tools have been developed at TUB. The uS-THERMANAL program is capable for the fast steady state and dynamic simulation of suspended multi-lavered ...

Keywords: Field solvers, Thermal simulation, Successive Network Reduction

222 Performance and reliability analysis of relevance filtering for scalable

distributed interactive simulation

Mostafa A. Bassiouni, Ming-Hsing Chiu, Margaret Loper, Michael Garnsey, Jim Williams

July 1997 ACM Transactions on Modeling and Computer Simulation (TOMACS), Volume 7 Issue 3

Publisher: ACM

Full text available: Rdf (499.11 KB) Additional Information: full citation, abstract, references, cited by, index terms

Bibliometrics: Downloads (6 Weeks): 5. Downloads (12 Months): 67. Citation Count: 8

Achieving the real-time linkage among multiple, geographically-distant, local area networks that support distributed interactive simulation (DIS) requires tremendous bandwidth and communication resources. Today, meeting the bandwidth and communication ...

Keywords: bandwidth reduction, distributed interactive simulation, real-time protocols, scalable algorithms

1 of 4 9/23/08 12:35 PM

223 Overview of microelectromechanical systems and design processes

William C. Tang

June 1997 DAC '97: Proceedings of the 34th annual conference on Design automation Publisher: ACM

Full text available: Pdf (35.57 KB) Additional Information: full citation, abstract, references, cited by index terms

Bibliometrics: Downloads (6 Weeks); 2. Downloads (12 Months); 41. Citation Count; 3

New design tools and automation strategies are neededto create robust, cost-effective, and manufacturable micro-machineddevices and systems. Some of the design automationissues include mixed-technology simulation, material property prediction in the micron-size ...

224 Structured design of microelectromechanical systems

Tamal Mukheriee, Garv K. Fedder

June 1997 DAC '97: Proceedings of the 34th annual conference on Design automation Publisher: ACM

Full text available: 🖷 Rdt (134.67 KB) Additional Information: full citation, abstract, references, cited by,

Bibliometrics: Downloads (6 Weeks); 7. Downloads (12 Months); 86. Citation Count; 8

In order to efficiently design complex microelectromechanical systems (MEMS) having large numbers of multi-domain components, a hierarchically structured design approach that is compatible with standard IC design is needed. A graphicalbasedschematic, or ...

225 CAD and foundries for microsystems

J. M. Karam, B. Courtois, H. Boutamine, P. Drake, A. Poppe, V. Szekely, M. Rencz, K. Hofmann, M. Glesner

June 1997 DAC '97: Proceedings of the 34th annual conference on Design automation Publisher: ACM

Full text available: 1 2df (304.07 KB) Additional Information: 1 1 citation, abstract, references, clied by, index terms

Bibliometrics: Downloads (6 Weeks): 4, Downloads (12 Months): 23, Citation Count: 4

Besides foundry facilities, Computer-Aided Design (CAD)tools are also required to move microsystems from research prototypes to an industrial market. Currently available CADtools need extensions before they can be used for theautomated design of micromachined ...

226 Microsystem Design Using Simulator Coupling

W. Wünsche, C. Clauß, P. Schwarz, F. Winkler

March 1997 EDTC '97: Proceedings of the 1997 European conference on Design and Test

Publisher: IEEE Computer Society

Full text available: Publisher Site , Ted (647.12 KB) Additional Information: full clistion, abstract. references

Bibliometrics: Downloads (6 Weeks): 1, Downloads (12 Months): 33, Citation Count: 0

2 of 4 9/23/08 12:35 PM The microsystem design process is characterized by interdisciplinary approaches and close interactions between different domains. A methodology for simulating the performance of complex microsystems using simulator coupling is presented. The technique ...

Keyw ords: circuit analysis computing, microsystem design process, simulator coupling, FEM program, ANSYS, circuit/system simulator, SABER, time step algorithm, acceleration sensor system, thermal interactions, integrated circuits

227 Sensors: the next wave of innovation

Paul Saffo

February 1997 Communications of the ACM, Volume 40 Issue 2

Publisher: ACM

Full text available: 南안 (192.99 KB) Additional Information: full citation, cited by, index terms

Bibliometrics: Downloads (6 Weeks): 7, Downloads (12 Months): 66, Citation Count: 6

228 Spotlights on recent developments in microsystem technology

S. Büttgenbach

September 1996 EURO-DAC '96/ EURO-VHDL '96: Proceedings of the conference on European design automation

Publisher: IEEE Computer Society Press

Full text available: Pdf (149.48 KB) Additional Information: full citation, references, index terms

Bibliometrics: Downloads (6 Weeks): 1, Downloads (12 Months): 36, Citation Count: 0

229 CAD of microsystems—a challenge for system engineering

K. Müller-Glaser

September 1996 EURO-DAC '96/ EURO-VHDL '96: Proceedings of the conference on European design automation

Publisher: IEEE Computer Society Press

Full text available: Pdt (29.10 KB) Additional Information: full citation, index terms

Bibliometrics: Downloads (6 Weeks); 2. Downloads (12 Months); 5. Citation Count; 0.

230 Design of a programmable temperature monitoring device for tagging small

Godi Fischer, James C. Daly, Chun Yang, Conrad W. Recksiek, Kevin D. Friedland August 1996 | SLPED '96: Proceedings of the 1996 international symposium on Low power electronics and design

Publisher: IEEE Press

Full text available: Pdf (294.13 KB) Additional Information: full citation, references, index terms

Bibliometrics: Downloads (6 Weeks): 2, Downloads (12 Months): 14, Citation Count: 0

231 Thermal test and monitoring [microelectronic structures]

V. Szekely, M. Rencz

3 of 4 9/23/08 12:35 PM March 1995 EDTC '95: Proceedings of the 1995 European conference on Design and

Publisher: IEEE Computer Society

Full text available: Publisher Site , Pdi (148.33 KB) Additional Information: full citation, abstract references, cited by

Bibliometrics: Downloads (6 Weeks): 4, Downloads (12 Months): 13, Citation Count: 1

We present possible solutions for the life-time thermal monitoring of microelectronic structures. In order to grab the thermal state of an encapsulated device, insertion of one or more temperature sensors is proposed. A new concept temperature sensor ...

Keywords: built-in test circuits, digital output signal, digital readout, electric sensing devices, encapsulated device, feedback oscillators, integrated circuit packaging, integrated circuit testing, life-time thermal monitoring, microelectronic structures, monitoring, sensor element, temperature measurement, temperature sensors, thermal-feedback oscillator

232 What have we learnt from using real parallel machines to solve real

oroblems? G. C. Fox

January 1989 Proceedings of the third conference on Hypercube concurrent computers and applications - Volume 2, volume 2

Publisher: ACM

Full text available: Pdf (4.08 MB) Additional Information: full citation, abstract, references, clied by, index terms

Bibliometrics: Downloads (6 Weeks): 27, Downloads (12 Months): 104, Citation Count: 13

We briefly review some key scientific and parallel processing issues in a selection of some 84 existing applications of parallel machines. We include the MIMD hypercube transputer array, BBN Butterfly, and the SIMD ICL DAP, Goodyear MPP and Connection ...

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501 Computational science and engineering

A. Sameh, G. Cybenko, M. Kalos, K. Neves, J. Rice, D. Sorensen, F. Sullivan December 1996 ACM Computing Surveys (CSUR), Volume 28 Issue 4 Publisher: ACM

Full text available: Pdf (188.49 KB) Additional Information: full citation, references, index terms

Bibliometrics: Downloads (6 Weeks): 12, Downloads (12 Months): 124, Citation Count: 0

502 CAD of microsystems—a challenge for system engineering

K. Müller-Glaser

September 1996 EURO-DAC '96/ EURO-VHDL '96: Proceedings of the conference on European design automation

Publisher: IEEE Computer Society Press

Full text available: Pdf (29.10 KB) Additional Information: full citation, index terms

Bibliometrics: Downloads (6 Weeks): 2, Downloads (12 Months): 5, Citation Count: 0

503 Design considerations and tools for low-voltage digital system design

Anantha Chandrakasan, Isabel Yang, Carlin Vieri, Dimitri Antoniadis June 1996 DAC '96: Proceedings of the 33rd annual conference on Design automation Publisher: ACM

Full text available: Redi (139.97 KB) Additional Information: full cliation, references, cited by, index terms

Bibliometrics: Downloads (6 Weeks): 0, Downloads (12 Months): 28, Citation Count: 8

504 Rapid design and manufacture of wearable computers

S. Finger, M. Terk, E. Subrahamanian, C. Kasabach, F. Prinz, D. P. Siewiorek, A. Smailagic, J. Stivoric, L. Weiss

February 1996 Communications of the ACM, Volume 39 Issue 2

Publisher: ACM

1 of 3 9/23/08 12:38 PM Full text available: Pdf (741.59 KB) Additional Information: full citation, references, cited by, index

Bibliometrics: Downloads (6 Weeks): 8. Downloads (12 Months): 59. Citation Count: 5

505 Power minimization in IC design! principles and applications

Massoud Pedram

January 1996 ACM Transactions on Design Automation of Electronic Systems (TODAES), Volume 1 Issue 1

Publisher: ACM

Full text available: Pdf (550.02 KB) Additional Information: full citation, abstract, references, cited by, index terms

Bibliometrics: Downloads (6 Weeks): 51, Downloads (12 Months): 470, Citation Count: 88

Low power has emerged as a principal theme in today's electronics industry. The need for low power has caused a major paradigm shift in which power dissipation is as important as performance and area. This article presents an in-depth survey of CAD methodologies ...

Keyw ords: CMOS circuits, adiabatic circuits, computer-aided design of VLSI, dynamic power dissipation, energy-delay product, gated clocks, layout, low power layout, low power synthesis, lower-power design, power analysis and estimation, power management, power minimization and management, probabilistic analysis, silicon-on-insulator technology, statistical sampling, switched capacitance, switching activity, symbolic simulation, synthesis, system design

506 The role of the personal computer in the design and development of advanced life-support equipment for tactical aircraft

Marty Pecaric

November 1995 CASCON '95: Proceedings of the 1995 conference of the Centre for Advanced Studies on Collaborative research

Publisher: IBM Press

Full text available: Pdf (118.81 KB) Additional Information: full citation, abstract, references, index terms

Bibliometrics: Downloads (6 Weeks): 0, Downloads (12 Months): 26, Citation Count: 0

Pilots of tactical aircraft are exposed to high accelerative forces. Acceleration in the headward direction (+ 62) will decrease blood flow to the head, causing changes in the visual field and/or loss of consciousness. G-protective life-support systems ...

507 Graphics rendering architecture for a high performance desktop workstation Chandlee B. Harrell, Farhad Fouladi

September 1993 SI GGRAPH '93: Proceedings of the 20th annual conference on Computer graphics and interactive techniques

Publisher: ACM

Full text available: Pdi (346.15 KB) Additional Information: full citation, references, cited by, index terms

Bibliometrics: Downloads (6 Weeks): 4. Downloads (12 Months): 66. Citation Count: 8

2 of 3 9/23/08 12:38 PM

508 An interdisciplinary laboratory for graphics research and applications

Donald P. Greenberg August 1977 ACM SIGGRAPH Computer Graphics, Volume 11 Issue 2 Publisher: ACM

Full text available: Pdf (12.55 MB) Additional Information: full cliation, abstract, references, cited by

Bibliometrics: Downloads (6 Weeks): 1. Downloads (12 Months): 31. Citation Count: 5

This paper describes the facilities and operation of the Program of Computer Graphics at Cornell University. A variety of graphic procedures are used for both input and output. The laboratory has the capability for producing dynamic vector displays and ...

509 An interdisciplinary laboratory for graphics research and applications

Donald P. Greenberg

July 1977 SI GGRAPH '77: Proceedings of the 4th annual conference on Computer graphics and interactive techniques

Publisher: ACM

Full text available: Full (12.55 MB) Additional Information; full citation, abstract, references, cited by

Bibliometrics: Downloads (6 Weeks): 1, Downloads (12 Months): 31, Citation Count: 5

This paper describes the facilities and operation of the Program of Computer Graphics at Cornell University. A variety of graphic procedures are used for both input and output. The laboratory has the capability for producing dynamic vector displays and ...

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761 <u>Practical performance/power alternatives within an existing CMOS technology generation</u>

Kerry Bernstein, John E. Bertsch, William F. Clark, John J. Ellis-Monaghan, Larry G. Heller, Edward J. Nowak

August 1996 I SLPED '96: Proceedings of the 1996 international symposium on Low power electronics and design

Publisher: IEEE Press

Full text available: Pdf (69.16 KB) Additional Information: full citation, references, index terms

Bibliometrics: Downloads (6 Weeks): 0, Downloads (12 Months): 10, Citation Count: 0

762 Relationships between fun and the computer business

Nolan Bushnell
August 1996 Communications of the ACM, Volume 39 Issue 8
Publisher: ACM

Full text available: Fdf (168.59 KB) Additional Information: Juli citation, cited by, index terms, tevers

Bibliometrics: Downloads (6 Weeks): 11, Downloads (12 Months): 88, Citation Count: 4

763 New paradigms for using computers

Ted Selker

August 1996 Communications of the ACM, Volume 39 Issue 8 Publisher: ACM

Full text available: 문화 문화 (532.80 KB) Additional Information: full citation, references, cited by, index learns, review

Bibliometrics: Downloads (6 Weeks): 3, Downloads (12 Months): 85, Citation Count: 3

764 Scheduling techniques to enable power management

José Monteiro, Srinivas Devadas, Pranav Ashar, Ashutosh Mauskar June 1996 DAC '96: Proceedings of the 33rd annual conference on Design automation

1 of 6 9/23/08 12:36 PM



Publisher: ACM

Full text available: Red (67.69 KB) Additional Information: full citation, references, cited by, index

Bibliometrics: Downloads (6 Weeks): 0, Downloads (12 Months): 22, Citation Count: 21

765 Automatic partitioning techniques for solving partial differential equations on

irregular adaptive meshes Jérôme Galtier

January 1996 ICS '96: Proceedings of the 10th international conference on Supercomputing

Publisher: ACM

Full text available: 🙌 Pdf (926.78 KB) Additional Information: full citation, references, bited by, Index terms

Bibliometrics: Downloads (6 Weeks): 0, Downloads (12 Months): 13, Citation Count: 1

766 Power minimization in IC design: principles and applications

Massoud Pedram January 1996 ACM Transactions on Design Automation of Electronic Systems (TODAES), Volume 1 Issue 1

Publisher: ACM

Full text available: 150.02 KB) Additional Information: tull citation, abstract, references, cited by, index terms

Bibliometrics: Downloads (6 Weeks): 51. Downloads (12 Months): 470. Citation Count: 88

Low power has emerged as a principal theme in today's electronics industry. The need for low power has caused a major paradigm shift in which power dissipation is as important as performance and area. This article presents an in-depth survey of CAD methodologies ...

Keywords: CMOS circuits, adiabatic circuits, computer-aided design of VLSI, dynamic power dissipation, energy-delay product, gated clocks, layout, low power layout, low power synthesis, lower-power design, power analysis and estimation, power management, power minimization and management, probabilistic analysis, silicon-on-insulator technology, statistical sampling, switched capacitance, switching activity, symbolic simulation, synthesis, system design

767 MONSTER-the ghost in the connection machine: modularity of neural

systems in theoretical evolutionary research

Nigel Snoad, Terry Bossomaier

December 1995 Supercomputing '95: Proceedings of the 1995 ACM/ IEEE conference on Supercomputing (CDROM) - Volume 00. Volume 00

Publisher: ACM

Full text available: in thin (42.44 KB) Additional Information: full citation, abstract, references, index terms

Bibliometrics: Downloads (6 Weeks): 1, Downloads (12 Months): 27, Citation Count: 0

Both genetic algorithms (GAs) and artificial neural networks (ANNs) (connectionist learning models) are effective generalisations of successful biological techniques to the artificial realm. Both techniques are inherently parallel and seem ideal for ...

Keywords: neural networks, genetic algorithms, Connection Machine (CM),

2 of 6

artificial life, image processing, neural modularity, evolutionary computation

768 Parallel matrix-vector product using approximate hierarchical methods

Ananth Grama, Vipin Kumar, Ahmed Sameh

December 1995 Supercomputing '95: Proceedings of the 1995 ACM/IEEE conference on Supercomputing (CDROM) - Volume 00, Volume 00

Publisher: ACM

Additional Information: full citation, abstract,

Full text available: Himi (2.40 KB), Pdf (659.96 KB)

references, cited by, index terms

Bibliometrics: Downloads (6 Weeks): 10, Downloads (12 Months): 18, Citation Count: 1

Matrix-vector products (mat-vecs) form the core of iterative methods used for solving dense linear systems. Often, these systems arise in the solution of integral equations used in electromagnetics, heat transfer, and wave propagation. In this paper, ...

769 Lattice QCD on the IBM scalable POWERParallel Systems SP2

C. Bernard, C. DeTar, S. Gottlieb, U. M. Heller, J. Hetrick, N. Ishizuka, L. Karkkainen, S. R. Lantz, K. Rummukainen, R. Sugar, D. Toussaint, M. Wingate

December 1995 Supercomputing '95: Proceedings of the 1995 ACM/IEEE conference on Supercomputing (CDROM) - Volume 00, Volume 00

Publisher: ACM

Additional Information: <u>juli sitation, abstract,</u>
B) references, index

Full text available: Him (3.03 KB), Pdf (266.17 KB)

terms
Bibliometrics: Downloads (6 Weeks): 2, Downloads (12 Months): 6, Citation Count: 0

A 512 node IBM Scalable POWERParallel Systems SP2 was installed at the Cornell Theory Center in October 1994. During the past couple of months we have been porting and optimizing code for carrying out lattice QCD calculations. Present performance is ...

770 Depicting fire and other gaseous phenomena using diffusion processes

Jos Stam, Eugene Fiume

September 1995 SIGGRAPH '95: Proceedings of the 22nd annual conference on Computer graphics and interactive techniques

Publisher: ACM

Additional Information: [uil citation,

Full text available: 🚰 Pdf (298.91 KB), 📓 Ps (134.43 KB)

references, cited by, index terms

Bibliometrics: Downloads (6 Weeks): 9. Downloads (12 Months): 128. Citation Count: 47

Keywords: advection, diffusion, fire, gaseous phenomena, light transport, multiple scattering, particle systems, smoke, turbulence, warped blobbies

771 1995 high level synthesis design repository

Preeti R. Panda, Nikil D. Dutt

September 1995 ISSS '95: Proceedings of the 8th international symposium on System synthesis

Publisher: ACM

3 of 6 9/23/08 12:36 PM

Additional Information: full citation, abstract, Full text available: Publisher Site , Pol (114.33 KB)

Additional Information: full citation, abstract, relearness, cited by, index terms

Bibliometrics: Downloads (6 Weeks): 3, Downloads (12 Months): 41, Citation Count: 22

Abstract: In this paper we briefly describe a set of designs that earn serve as examples for high level synthesis (HLS) systems. The designs vary in complexity from simple behavioral finite state machines to more complex designs such as microprocessors ...

Keywords: 1995 high level synthesis design repository, VHDL language, behavioral finite state machines, behavioral level, computational complexity, finite state machines, floating point units, hardware description languages, high level synthesis, microprocessor chips, microprocessors

772 Efficient support for irregular applications on distributed-memory machines

Shubhendu S. Mukherjee, Shamik D. Sharma, Mark D. Hill, James R. Larus, Anne Rogers, Joel Saltz

August 1995 PPOPP '95: Proceedings of the fifth ACM SIGPLAN symposium on Principles and practice of parallel programming

Publisher: ACM

Full text available: Post (1.36 MB) Additional Information: full citation, abstract, references, cited by, index terms

Bibliometrics: Downloads (6 Weeks): 1. Downloads (12 Months): 38. Citation Count: 28

Irregular computation problems underlie many important scientific applications. Although these problems are computationally expensive, and so would seem appropriate for parallel machines, their irregular and unpredictable run-time behavior makes this ...

773 Efficient support for irregular applications on distributed-memory machines

Shubhendu S. Mukherjee, Shamik D. Sharma, Mark D. Hill, James R. Larus, Anne Rogers, Joel Saltz

August 1995 ACM SIGPLAN Notices, Volume 30 Issue 8

Publisher: ACM

Full text available: Fdf (1.36 MB) Additional Information: full cliation, abstract, references, cited by, index terms

Bibliometrics: Downloads (6 Weeks): 1, Downloads (12 Months): 38, Citation Count: 28

Irregular computation problems underlie many important scientific applications. Although these problems are computationally expensive, and so would seem appropriate for parallel machines, their irregular and unpredictable run-time behavior makes this ...

774 Overview of the power minimization techniques employed in the IBM

PowerPC 4xx embedded controllers

Anthony Correale, Jr.

April 1995 ISLPED '95: Proceedings of the 1995 international symposium on Low

Publisher: ACM

ubilistiet. Acivi

Full text available: Pdf (44.29 KB) Additional Information: tuli citation, references, cited by, index

Bibliometrics: Downloads (6 Weeks): 12. Downloads (12 Months): 32. Citation Count: 6

775 A simple graph-based intermediate representation

Cliff Click, Michael Paleczny

March 1995 ACM SIGPLAN Notices, Volume 30 Issue 3

Publisher: ACM

Full text available: Pdf (157.59 KB) Additional Information: tull citation, abstract, references, cited by, index terms

Bibliometrics: Downloads (6 Weeks): 7, Downloads (12 Months): 50, Citation Count: 2

We present a graph-based intermediate representation (IR) with simple semantics and a low-memory-cost C++ implementation. The IR uses a directed graph with labeled vertices and ordered inputs but unordered outputs. Vertices are labeled with opcodes, ...

776 A simple graph-based intermediate representation

Cliff Click, Michael Paleczny

March 1995 Papers from the 1995 ACM SIGPLAN workshop on Intermediate representations

Publisher: ACM

Full text available: 157.59 KB) Additional Information: full citation, abstract, references, clied by, index terms

Bibliometrics: Downloads (6 Weeks): 7, Downloads (12 Months): 50, Citation Count: 2

We present a graph-based intermediate representation (IR) with simple semantics and a low-memory-cost C++ implementation. The IR uses a directed graph with labeled vertices and ordered inputs but unordered outputs. Vertices are labeled with opcodes, ...

777 Hardware assists for high performance computing using a mathematics of

arrays

H. Pottinger, W. Eatherton, J. Kelly, T. Schiefelbein, L. R. Mullin, R. Ziegler February 1995 FPGA '95: Proceedings of the 1995 ACM third international symposium on Field-programmable gate arrays

Publisher: ACM

Full text available: 🖷 Pdf (149.38 KB) Additional Information: juli citation, abstract, references, index terms

Bibliometrics: Downloads (6 Weeks): 6. Downloads (12 Months): 21. Citation Count: 0

Work in progress at the University of Missouri-Rolla on hardware assists for high performance computing is presented. This research consists of a novel field programmable gate array (FPGA) based reconfigurable coprocessor board (the Chameleon Coprocessor) ...

5 of 6 9/23/08 12:36 PM

778 Leo: a system for cost effective 3D shaded graphics

Michael F. Deering, Scott R. Nelson

September 1993 SI GGRAPH '93: Proceedings of the 20th annual conference on Computer graphics and interactive techniques

Publisher: ACM

Full text available: Pdf (241.27 KB) Additional Information: ull citation, references, clied by, index

Bibliometrics: Downloads (6 Weeks): 1, Downloads (12 Months): 36, Citation Count: 19

Keywords: 3D graphics hardware, antialiased lines, floating-point microprocessors, gouraud shading, parallel graphics algorithms, rendering

779 Computing perspectives: the rise of the VLSI processor

Maurice V. Wilkes

December 1990 Communications of the ACM, Volume 33 Issue 12 Publisher: ACM

Full text available: 南안 (504.95 KB) Additional Information: full citation, abstract, index terms

Bibliometrics: Downloads (6 Weeks): 3, Downloads (12 Months): 29, Citation Count: 0

Around 1970 Intel discovered it could put 2,000 transistors-or perhaps a few more-on a single NMOS chip. In retrospect, this may be said to mark the beginning of very large-scale integration (VLSI), an event which had been long heralded, ...

780 A linear array of processors with partially shared memory for parallel solution of PDE

Yoshikuni Okawa, Naoki Haraguchi

November 1990 Supercomputing '90: Proceedings of the 1990 ACM/IEEE conference on Supercomputing

Publisher: IEEE Computer Society

Full text available: Pdf (690.38 KB) Additional Information: full citation, abstract, references

Bibliometrics: Downloads (6 Weeks): 2, Downloads (12 Months): 3, Citation Count: 0

We propose a multiprocessor architecture with partially shared memory blocks, which is, we think, best suited for the successive approximation of scientific computing problems, such as matrix operations, partial differential equations etc. The topology ...

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